



晶采光電科技股份有限公司  
AMPIRE CO., LTD.

# SPECIFICATIONS FOR LCD MODULE

<b>CUSTOMER</b>	
<b>CUSTOMER PART NO.</b>	
<b>AMPIRE PART NO.</b>	<b>AM-800480RSTMQW-TAXH</b>
<b>APPROVED BY</b>	
<b>DATE</b>	

- Approved For Specifications  
 Approved For Specifications & Sample

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## RECORD OF REVISION

Revision Date	Page	Contents	Editor
2017/03/16	-- --	New Release	Mark

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## 1. Features

7 inch module is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit , a back light system and capacitive touch panel . This TFT LCD has a 7.0 (16:9) inch diagonally measured active display area with WVGA (800 horizontal by 480 vertical pixel) resolution.

- (1) Construction: 7" a-Si TFT active matrix, White LED Backlight.
- (2) Resolution (pixel): 800(R.G.B) X 480.
- (3) Number of the Colors : 262K colors ( R , G , B 6 bit digital each).
- (4) LCD type : Transmissive , normally White.
- (5) Viewing Direction 6 O'clock.
- (6) LVDS Interface.
- (7) Built-in LED driver.
- (8) Touch Panel with I2C interface. Touch controller : EXC7200 ( EETI)
- (9) 800480RL Cover Lens (174.0\*118.0\*4.0mm)
- (10) Sunlight readable by Nano optical lamination

## 2. PHYSICAL SPECIFICATIONS

Item	Specifications	unit
LCD size	7 inch (Diagonal)	
Outline Dimension (Include connecter)	174x 118 x 11.965 (typ.)	mm
Number of Pixel	800 RGB (H) x 480 (V)	pixels
Pixel pitch	0.1905 (H) x 0.1905 (V)	mm
Pixel arrangement	RGB Vertical stripe	
Display mode	Normally white	
Back-light	Single LED (Side-Light type)	
Brightness	425 nit	cd/m <sup>2</sup>
Contrast ratio	400 : 1	
Power consumption (B/L system)	4 (max.)	W

### 3. ABSOLUTE MAX. RATINGS

#### 3-1 Electrical Absolute Rating

##### 3-1-1 TFT LCD Module

Item	Symbol	Values		UNIT	Note
		Min.	Max.		
Power voltage	VDD	-0.5	4.0	V	GND=0V
Voltage range at any terminal		-0.5	VDD+0.3	V	

#### 3-2 Environment Absolute Rating

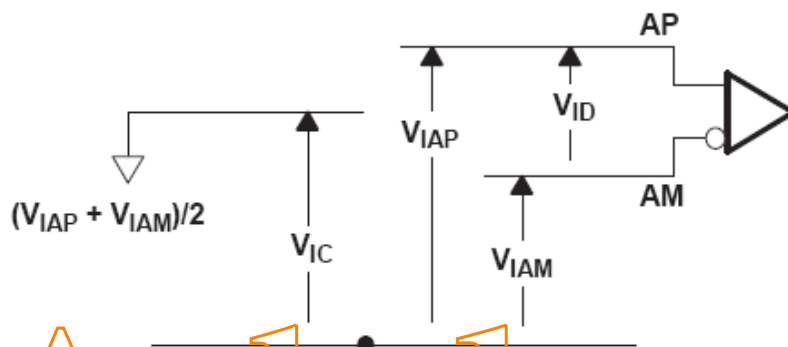
Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	Topa	-20	70	°C	
Storage Temperature	Tstg	-30	80	°C	

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## 4. ELECTRICAL CHARACTERISTICS

### 4-1 TFT LCD Module voltage

		MIN	NOM	MAX	UNIT
$V_{CC}$	Supply voltage	3	3.3	3.6	V
$V_{IH}$	High-level input voltage ( $\overline{SHTDN}$ )	2			V
$V_{IL}$	Low-level input voltage ( $\overline{SHTDN}$ )			0.8	V
$ V_{ID} $	Magnitude differential input voltage	0.1		0.6	V
$V_{IC}$	Common-mode input voltage	$\frac{ V_{ID} }{2}$		$2.4 - \frac{ V_{ID} }{2}$	V



ITEM	SYMBOL	MIN	TYP	MAX	UNIT	CONDITION
Power Supply Voltage For LCD	$V_{CC}$	3.0	3.3	3.6	V	
Power Supply Current For LCD	$I_{CC}$	-	180	230	mA	Black pattern

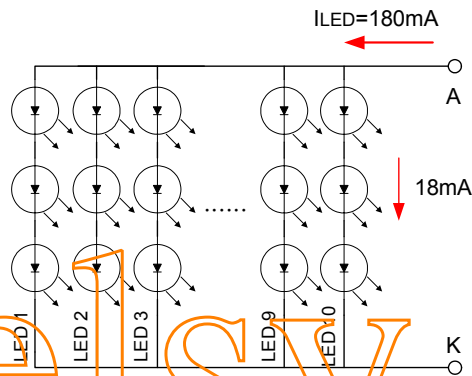
## 4-2 Backlight Unit

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	CONDITION
LED Backlight Voltage	$V_{BL}$	8.4	9.0	10.8	V	For reference
LED Backlight Current	$I_{BL}$	-	180	-	mA	$T_a=25^{\circ}\text{C}$
LED Life Time	-	--	40K	--	Hour	Note (2)

Note (1) The constant current source is needed for white LED back-light driving.

When LCM is operated over  $60^{\circ}\text{C}$  ambient temperature, the  $I_{LED}$  of the LED back-light should be adjusted to 135mA max

Note (2) Brightness to be decreased to 50% of the initial value,  $T_a=25^{\circ}\text{C}$ .



LED Light Bar Circuit

Note(3) VLEDADJ is PWM signal input. It is for brightness control.

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
ADJ signal frequency	$f_{PWM}$	100	50K	200K	Hz
ADJ signal logic level High	$V_{IH}$	2V	--	VLED (5.0V)	V
ADJ signal logic level Low	$V_{IL}$	0	--	0.5	V

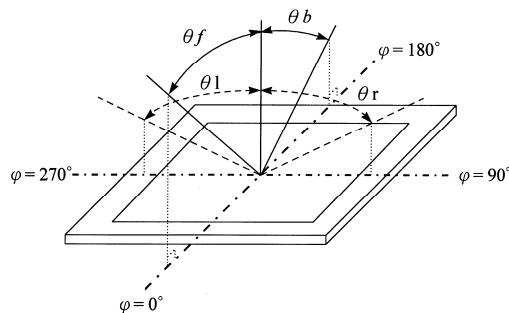
## 5. Optical Specifications

### 5-1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle	Front	$\theta f$	$CR \geq 10$	55	60	--	deg.	(1)(2)(3)
	Back	$\theta b$		55	70	--		
	Left	$\theta l$		65	70	--		
	Right	$\theta r$		65	70	--		
Contrast ratio		CR	$\Theta = \Phi = 0^\circ$	250	400	--	--	(1)(3)
Response Time		$T_r$	$\Theta = \Phi = 0^\circ$	--	5	10	ms	(1)(4)
		$T_f$		--	11	16	ms	(1)(4)
Color chromaticity	White	$W_x$	$\Theta = \Phi = 0^\circ$	0.239	0.299	0.359	--	(1)
		$W_y$		0.268	0.328	0.388		
Luminance		L	$\Theta = \Phi = 0^\circ$	340	425	--	cd/m <sup>2</sup>	(1)(5)
Luminance Uniformity		$\Delta L$	$\Theta = \Phi = 0^\circ$	70	--	--	%	(1)(5)(6)

Note 1:  $T_a = 25^\circ\text{C}$ . To be measured on the center area of panel after 10 minutes operation.

Note 2: Definition of Viewing Angle



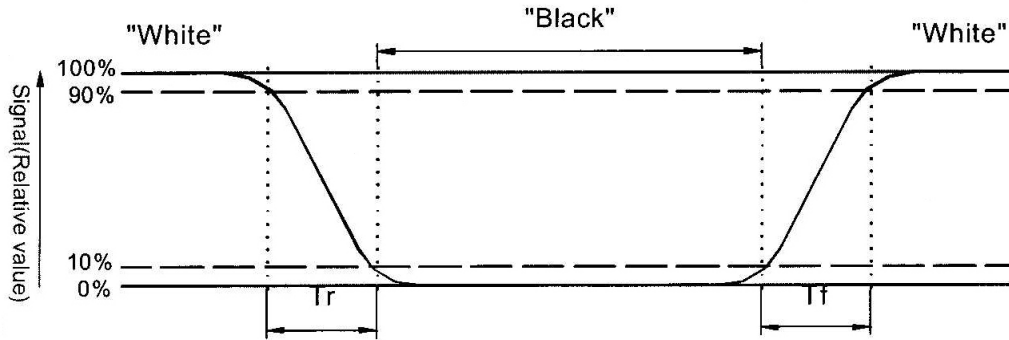
Note 3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

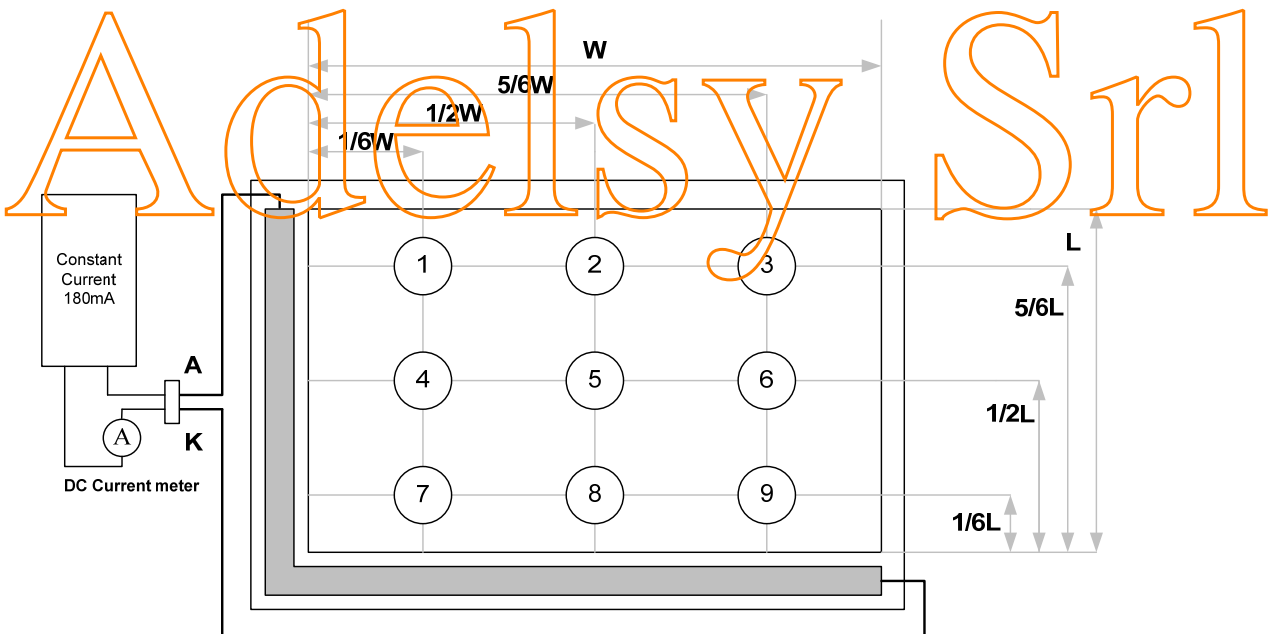
$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector Output when LCD is at "Black" state}}$$

Note 4: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time) respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 5 : Luminance is measured at point 5 of the display.



Note 6 : Definition of Luminance Uniformity

$$\Delta L = [ L(\text{min.}) \text{ of } 9 \text{ points} / L(\text{max.}) \text{ of } 9 \text{ points}] \times 100\%$$



## 6. Touch Panel SPECIFICATION

### 6.1 Basic Characteristic

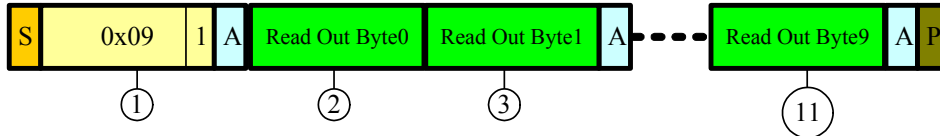
ITEM	SPECIFICATION
Type	Projective Capacitive Touch Panel
Activation	Two-fingers or Signal-finger
X/Y Position Reporting	Absolute Position
Touch Force	No contact pressure required
Calibration	No need for calibration
Report Rate	Approx 100 points/sec
Control IC	EETI EXC7200 +EX5404

### 6.2 Interface

CN6		
Pin No.	Symbol	Function
1	DGND	USB POWER GND
2	SDA	I2C DATA
3	SCL	I2C CLOCK
4	VDD	5V/3.3V Regulated
5	IRQ	Interrupt Request pin
6	RST	Reset pin to Master Chip

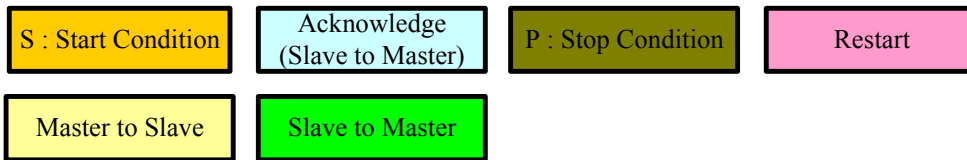
### 6.3 7-bit I2C address = 0x04.

The complete I2C data format:



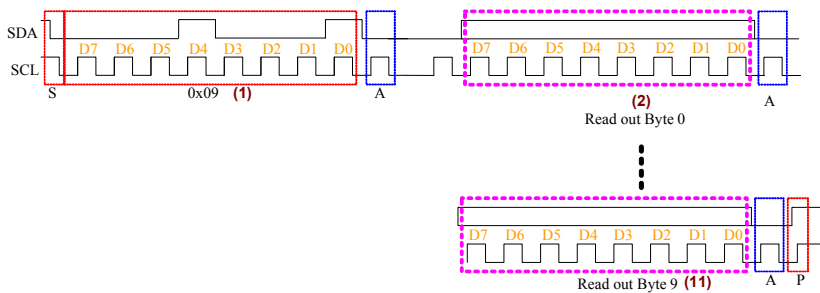
① // 7bit I2C address =0x04 Send I2C Slave address 0x04 Bit1=1 for:write

② ~ ⑪ // Read out Byte0~byte10



The detail Timing

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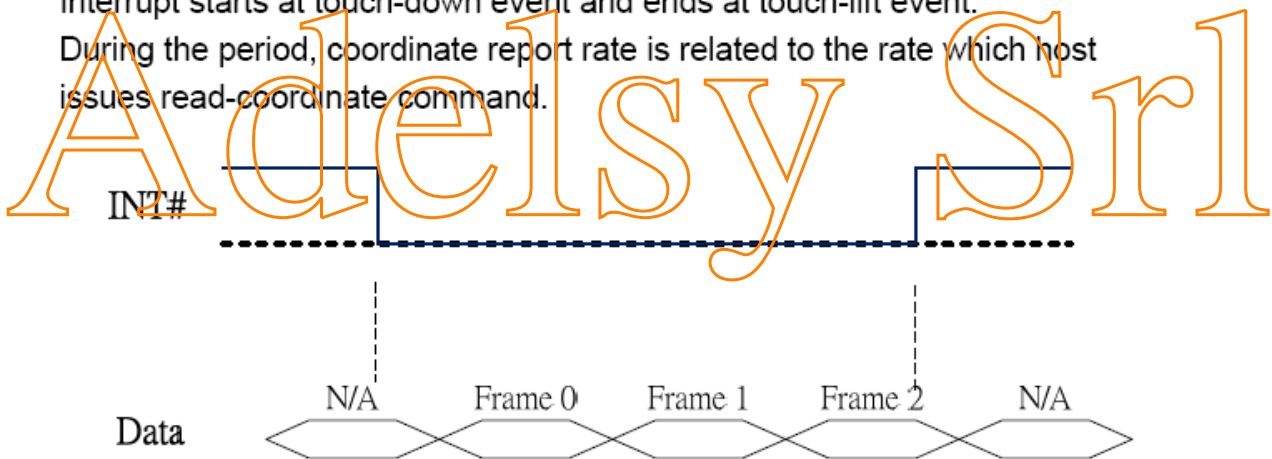


Read Out Byte	BIT7	BIT6	BIT5	BIT4	BIT3	BIT2	BIT1	BIT0
BYTE0	0	0	0	0	0	1	0	0
Report ID = 0x04								
BYTE1								
BIT[7] : When this bit =1 is Valid touch. BIT[6:2] : Contact ID. BIT[1] : In range bit , this bit is always 1. BIT[0] : This bit 1 for touch down , 0 for touch lift Example: 0x83: 1 <sup>st</sup> Touch Down.								

	0x82: 1 <sup>st</sup> Touch Lift. 0x87: 2nd Touch Down. 0x86: 2nd Touch Lift.	
BYTE2	Touch X [3:0]	Don't care
	X Coordination Bit [3:0] in BYTE2 Bit [ 7: 4]	
BYTE3	Touch X [15:4]	
	X Coordination Bit [15:4] in BYTE3	
BYTE4	Touch Y [3:0]	Don't care
	Y Coordination Bit [3:0] in BYTE2 Bit [ 7: 4]	
BYTE5	Touch Y [15:4]	
	Y Coordination Bit [15:4] in BYTE3	
BYTE6		
	Reserved	
BYTE7		
	Reserved	
BYTE8		
	Reserved	
BYTE9		
	Reserved	

Interrupt starts at touch-down event and ends at touch-lift event.

During the period, coordinate report rate is related to the rate which host issues read-coordinate command.



**Coordinate**

Origin (0,0)



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Sample Code :

```

/*****
* Function Name   : u8 EXC7200_I2C_CoordRead(u8 Slave_Addr,u8 read_Nbyte , u16 *ByteReturn)
* Description    : Use GPIO Read N byte data from Slave device (Addr) to Host
* Input         : u8 Slave_Addr , Ex:0x04
*               : u8 read_Nbyte
*               : Point for touch information
*               : ByteReturn [0] = Point X1
*               : ByteReturn [1] = Point Y1
*               : ByteReturn [2] = T.B.D
*               : ByteReturn [3] = Point Y2
*               : ByteReturn [4] = Point Y2
*               : ByteReturn [5] = T.B.D
* Return
*
* #define EXC7200_I2C_CoordRead_1stTouch_Down  0x01
* #define EXC7200_I2C_CoordRead_1stTouch_Lift  0x81
* #define EXC7200_I2C_CoordRead_2ndTouch_Down  0x02
* #define EXC7200_I2C_CoordRead_2ndtTouch_Lift  0x82
*****/

```

```

#define EXC7200_I2C_CoordRead_1stTouch_Down  0x01
#define EXC7200_I2C_CoordRead_1stTouch_Lift  0x81
#define EXC7200_I2C_CoordRead_2ndTouch_Down  0x02
#define EXC7200_I2C_CoordRead_2ndtTouch_Lift  0x82
#define EXC7200_I2C_CoordRead_error  0x00

```

```

u8 EXC7200_I2C_CoordRead(u8 Slave_Addr,u8 read_Nbyte , u16 *ByteReturn )
{
    u8 *pBuffer ,i;
    u8 Byte[10];

    IO_I2C_start(); // Start Condition
    IO_I2C_reg_cmd_para((Slave_Addr<<1)+1); // Send I2C Slave address+1 Bit0=1 for:read
    pBuffer=IO_I2C_read_Nbyte(read_Nbyte); // read 10 byte

    for(i=0;i<10;i++)
    {
        Byte[i]=*pBuffer;
        pBuffer++;
    }

    if( (Byte[1]==0x83) | (Byte[1]==0x82)) //
    {
        ByteReturn[0]=((u16)((Byte[3]&0x00ff)<<4))+((u16)((Byte[2]&0x00f0)>>4)); //Point X1
        ByteReturn[1]=((u16)((Byte[5]&0x00ff)<<4))+((u16)((Byte[4]&0x00f0)>>4)); //Point Y1
        ByteReturn[2]= 0xFFFF;
        ByteReturn[3]= 0xFFFF;
        ByteReturn[4]= 0xFFFF;
        ByteReturn[5]= 0xFFFF;
        Previous_X1=ByteReturn[0];
        Previous_Y1=ByteReturn[1];

        if ( (Byte[1]==0x83))
        {
            return EXC7200_I2C_CoordRead_1stTouch_Down;
        }
        if ( (Byte[1]==0x82))
        {

```

```

        return EXC7200_I2C_CoordRead_1stTouch_Lift;
    }
}

if( (Byte[1]==0x87) | (Byte[1]==0x86)) //
{
    ByteReturn[3]=((u16)((Byte[3]&0x00ff)<<4))+((u16)((Byte[2]&0x00f0)>>4)); //Point X1
    ByteReturn[4]=((u16)((Byte[5]&0x00ff)<<4))+((u16)((Byte[4]&0x00f0)>>4)); //Point Y1
    ByteReturn[5]= 0xFFFF;
    ByteReturn[0]= Previous_X1;
    ByteReturn[1]= Previous_Y1;
    ByteReturn[2]= Previous_Z1;

    if ( (Byte[1]==0x87))
    {
        return EXC7200_I2C_CoordRead_2ndTouch_Down;
    }
    if ( (Byte[1]==0x86))
    {
        return EXC7200_I2C_CoordRead_2ndTouch_Lift;
    }
}

return EXC7200_I2C_CoordRead_error;
}

```

```

// Example Interrupt function
void EXC7200_I2C_EXT_INT (void)
{

```

```

    u16 DataBuffer[10];
    u32 TPX1,TPY1,TPX2,TPY2;
    u16 Temp_X=0xFFFF,Temp_Y=0xFFFF;
    u16 temp;
    u8 Touch_size=4;
    u8 RStatus;

```

```

    while((ReadINT1())==0)
    {

```

```

        RStatus = EXC7200_I2C_CoordRead(0x04,10,DataBuffer);

```

```

        TPX1=(u16) DataBuffer[0]; //first X position
        TPY1=(u16) DataBuffer[1]; //first Y position
        TPX2=(u16) DataBuffer[3]; //second point X position
        TPY2=(u16) DataBuffer[4]; //second point Y position

```

```

        // Remapping Touch X,Y to LCD X,Y
        TPX1*=Current_LCM_ID.LCD_X_Max;
        TPX1/=2048;
        TPY1*=Current_LCM_ID.LCD_Y_Max;
        TPY1/=2048;
        TPX2*=Current_LCM_ID.LCD_X_Max;
        TPX2/=2048;
        TPY2*=Current_LCM_ID.LCD_Y_Max;

```

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```

TPY2/=2048;

switch (RStatus)
{
    case (EXC7200_I2C_CoordRead_1stTouch_Down):
GUI_CircleFill(TPX1, TPY1, 2, rand()%0xFFFF);
        // Do 1st touch down Function
        break;
    case (EXC7200_I2C_CoordRead_1stTouch_Lift):
GUI_RectangleFill(TPX1-4, TPY1-4,TPX1+4, TPY1+4 ,rand()%0xFFFF);
        // Do 1st touch Lift Function

        break;
    case (EXC7200_I2C_CoordRead_2ndTouch_Down):
GUI_CircleFill(TPX1, TPY1, 2, RGB(128,128,128));
GUI_CircleFill(TPX2, TPY2, 2, RGB(128,128,0));
        // Do 2nd touch Down Function
        break;
    case (EXC7200_I2C_CoordRead_2ndTouch_Lift):
GUI_RectangleFill(TPX1-4, TPY1-4,TPX1+4, TPY1+4 ,RGB(128,128,128));
GUI_RectangleFill(TPX2-4, TPY2-4,TPX2+4, TPY2+4 ,RGB(128,128,0));
        // Do 2nd touch Lift Function
        break;
    default:
        break;
}

```

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## 7. INTERFACE

### 7-1 Touch Panel Interface

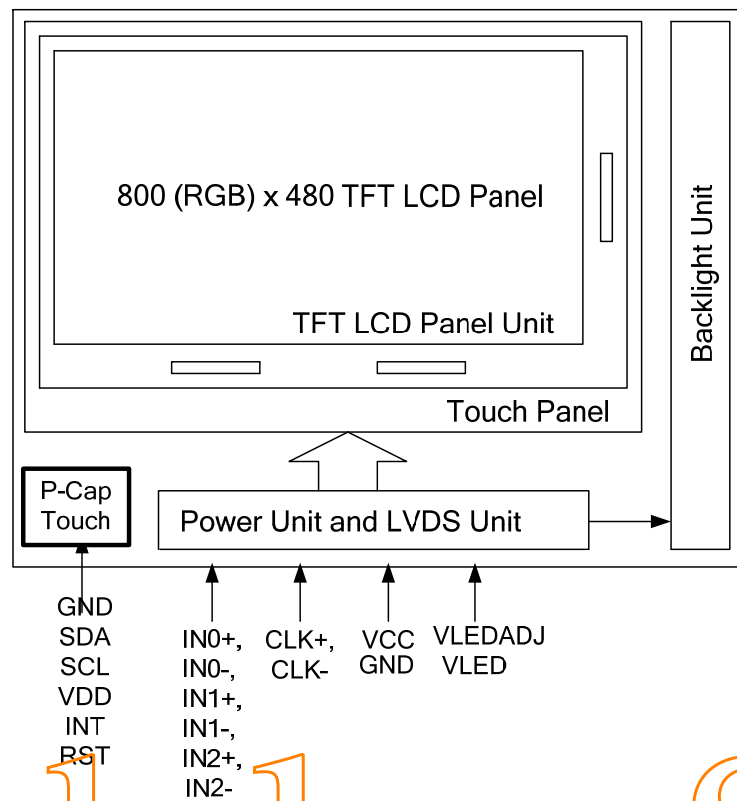
Pin No.	Symbol	Function
1	DGND	USB POWER GND
2	SDA	I2C DATA
3	SCL	I2C CLOCK
4	VDD	5V/3.3V Regulated
5	IRQ	Interrupt Request pin
6	RST	Reset pin to Master Chip

### 7-2 LCD Control Interface

Pin No.	Symbol	Function
1	VDD	POWER SUPPLY:3.3V
2	VDD	POWER SUPPLY:3.3V
3	GND	Power Ground
4	GND	Power Ground
5	IN0-	Transmission Data of Pixels
6	IN0+	Transmission Data of Pixels
7	GND	Power Ground
8	IN1-	Transmission Data of Pixels 1
9	IN1+	Transmission Data of Pixels 1
10	GND	Power Ground
11	IN2-	Transmission Data of Pixels 2
12	IN2+	Transmission Data of Pixels 2
13	GND	Power Ground
14	CLK-	Sampling Clock
15	CLK+	Sampling Clock
16	GND	Power Ground
17	VLED	POWER SUPPLY for Backlight : <b>5V</b>
18	VLED	POWER SUPPLY for Backlight : <b>5V</b>
19	GND	Power Ground
20	ADJ	PWM SIGNAL



### 7-3 Block Diagram



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## 8. INPUT SIGNAL :

### 8-1 LVDS Signal

switching characteristics over recommended operating conditions (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP†	MAX	UNIT
$t_{su}$	Setup time, D0–D20 to CLKOUT↓	5			ns
$t_h$	Data hold time, CLKOUT↓ to D0–D20	5			ns
$t_{(RSKM)}$	Receiver input skew margin§ (see Figure 7)	550	700		ps
$t_d$	Delay time, CLKIN↑ to CLKOUT↓ (see Figure 7)	3	5	7	ns
$t_{en}$	Enable time, $\overline{SHTDN}$ to phase lock		1		ms
$t_{dis}$	Disable time, $\overline{SHTDN}$ to off state		400		ns
$t_t$	Transition time, output (10% to 90% $t_r$ or $t_f$ ) (data only)		3		ns
$t_t$	Transition time, output (10% to 90% $t_r$ or $t_f$ ) (clock only)		1.5		ns
$t_w$	Pulse duration, output clock		$0.50 t_c$		ns

† All typical values are at  $V_{CC} = 3.3\text{ V}$ ,  $T_A = 25^\circ\text{C}$ .

§ The parameter  $t_{(RSKM)}$  is the timing margin available to allocate to the transmitter and interconnection skews and clock jitter. The value of this parameter at clock periods other than 15.38 ns can be calculated from  $t_{(RSKM)} = tc/14 - 550\text{ ps}$ .

¶  $|\text{Input clock jitter}|$  is the magnitude of the change in input clock period.

#### PARAMETER MEASUREMENT INFORMATION

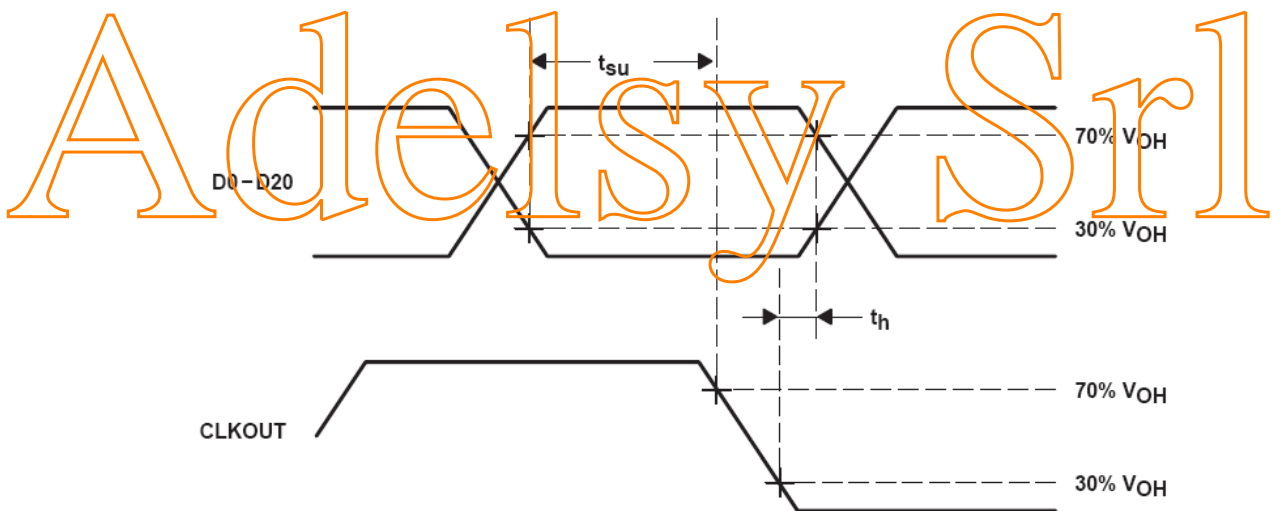


Figure 5. Setup and Hold Time Waveforms

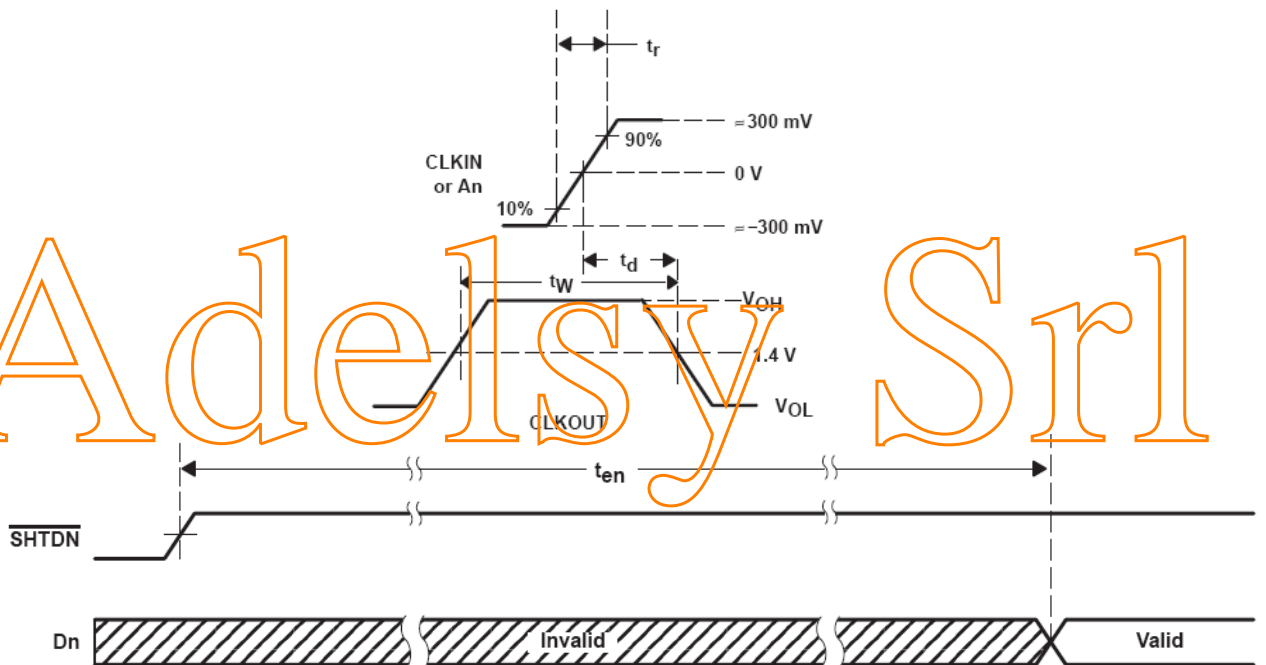
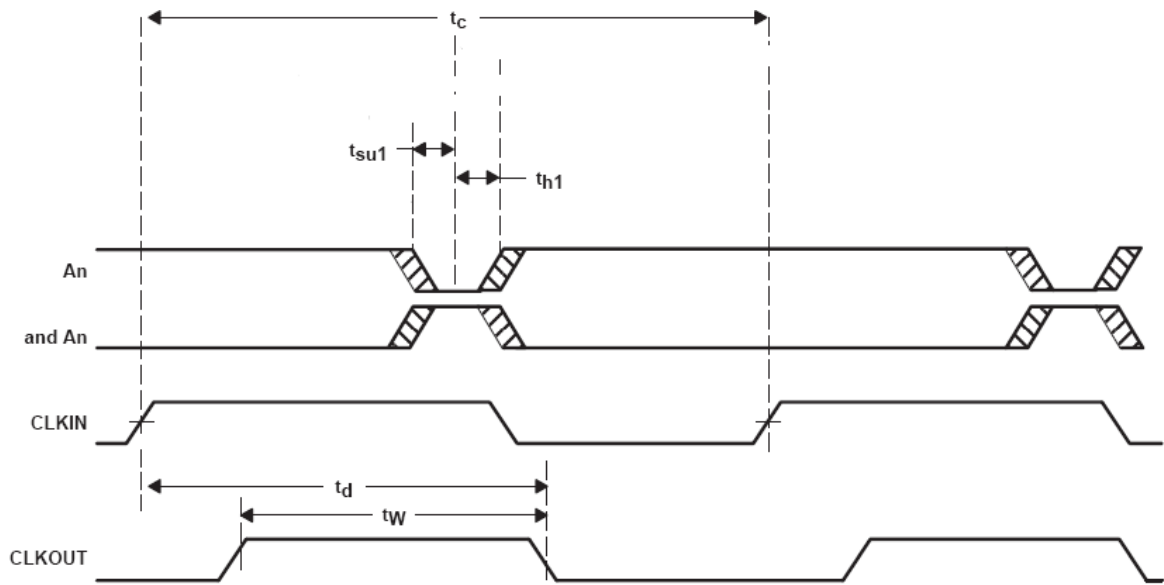


Figure 7. Enable Time Waveforms

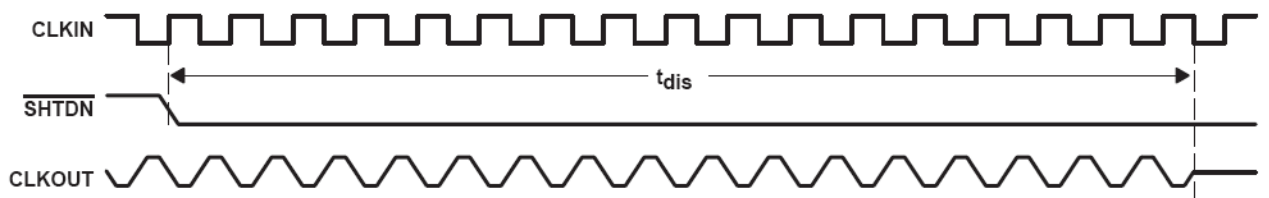
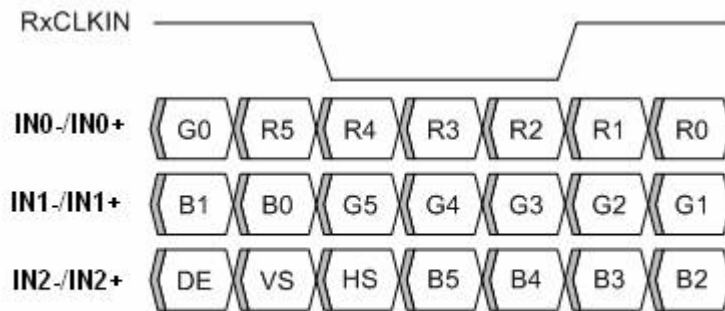


Figure 8. Disable Time Waveforms

## LVDS Input Data Format



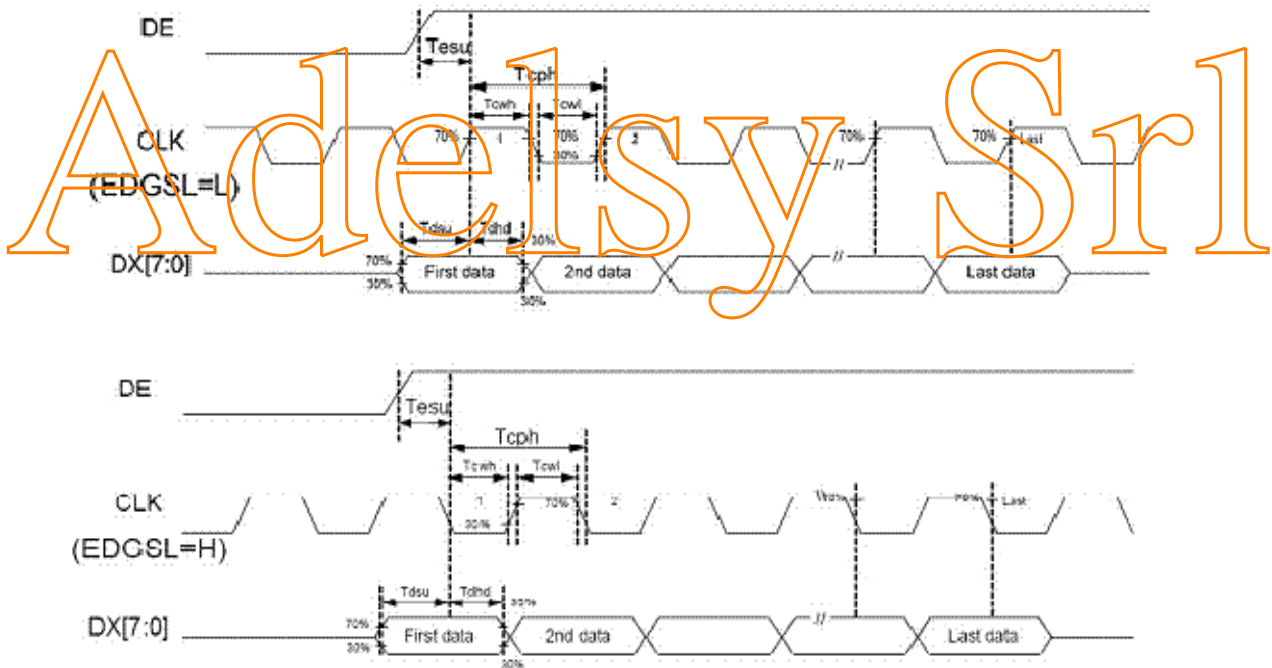
Note : R/G/B data 5 : MSB, R/G/B data 0 : LSB

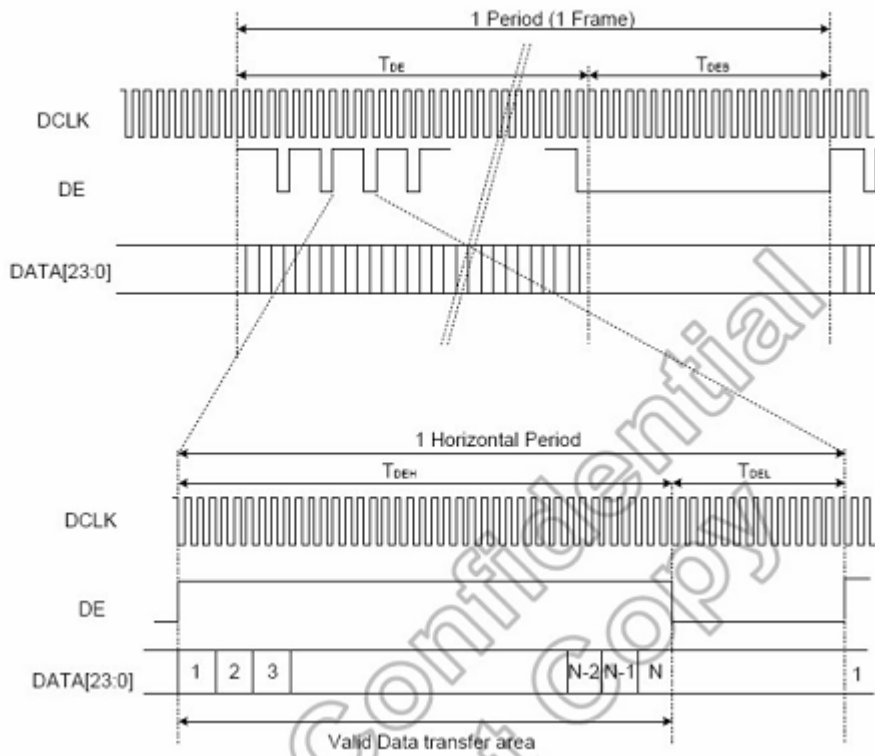
Signal Name	Description	Remark
R5 R4 R3 R2 R1 R0	Red Data 5 (MSB) Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0 (LSB)	Red-pixel Data Each red pixel's brightness data consists of these 6 bits pixel data.
G5 G4 G3 G2 G1 G0	Green Date 5 (MSB) Green Date 4 Green Date 3 Green Date 2 Green Date 1 Green Date 0 (LSB)	Green-pixel Data Each green pixel's brightness data consists of these 6 bits pixel data.
B5 B4 B3 B2 B1 B0	Blue Data 5 (MSB) Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0 (LSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 6 bits pixel data.
RxCLKIN+ RxCLKIN-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

## 8.2 Timing characteristic of Panel

Parameter	Symbol				Unit
		Min.	Typ.	Max.	
Data setup time	T <sub>dsu</sub>	6	-	-	ns
Data hold time	T <sub>dhd</sub>	6	-	-	T <sub>cph</sub>
DE setup time	T <sub>esu</sub>	6	-	-	T <sub>cph</sub>
CLK frequency	F <sub>CPH</sub>		33.26		MHz
CLK period	T <sub>CPH</sub>		30.06		ns
CLK pulse duty	T <sub>CWH</sub>	40	50	60	%
DE period	T <sub>DEH</sub> +T <sub>DEL</sub>	1000	1056	1200	T <sub>CPH</sub>
DE pulse width	T <sub>DEH</sub>	-	800	-	T <sub>CPH</sub>
DE frame blanking	T <sub>DEB</sub>	10	45	110	T <sub>DEH</sub> +T <sub>DEL</sub>
DE frame width	T <sub>DE</sub>	-	480	-	T <sub>DEH</sub> +T <sub>DEL</sub>

Note : We suggest using the typical value, so it can have better performance.





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## 9. DISPLAYED COLOR AND INPUT DATA

	Color & Gray Scale	DATA SIGNAL																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(31)	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(62)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(31)	0	0	0	0	0	0	0	1	1	1	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(31)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

## 10. Reliability Test Items

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	60°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min) ~ 70°C (30min) 100 cycles	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions  
(15-35°C , 45-65%RH).

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## 11. USE PRECAUTIONS

### 11.1 Handling precautions

- 1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
- 2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
- 3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
- 4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

### 11.2 Installing precautions

- 1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. 1MΩ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- 2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- 3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
- 4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

### 11.3 Storage precautions

- 1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- 2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
- 3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

### 11.4 Operating precautions

- 1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- 2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- 3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC drive voltage.
- 4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- 5) Make certain that each signal noise level is within the standard (L level: 0.2V<sub>dd</sub> or less and H level: 0.8V<sub>dd</sub> or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- 6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- 7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.

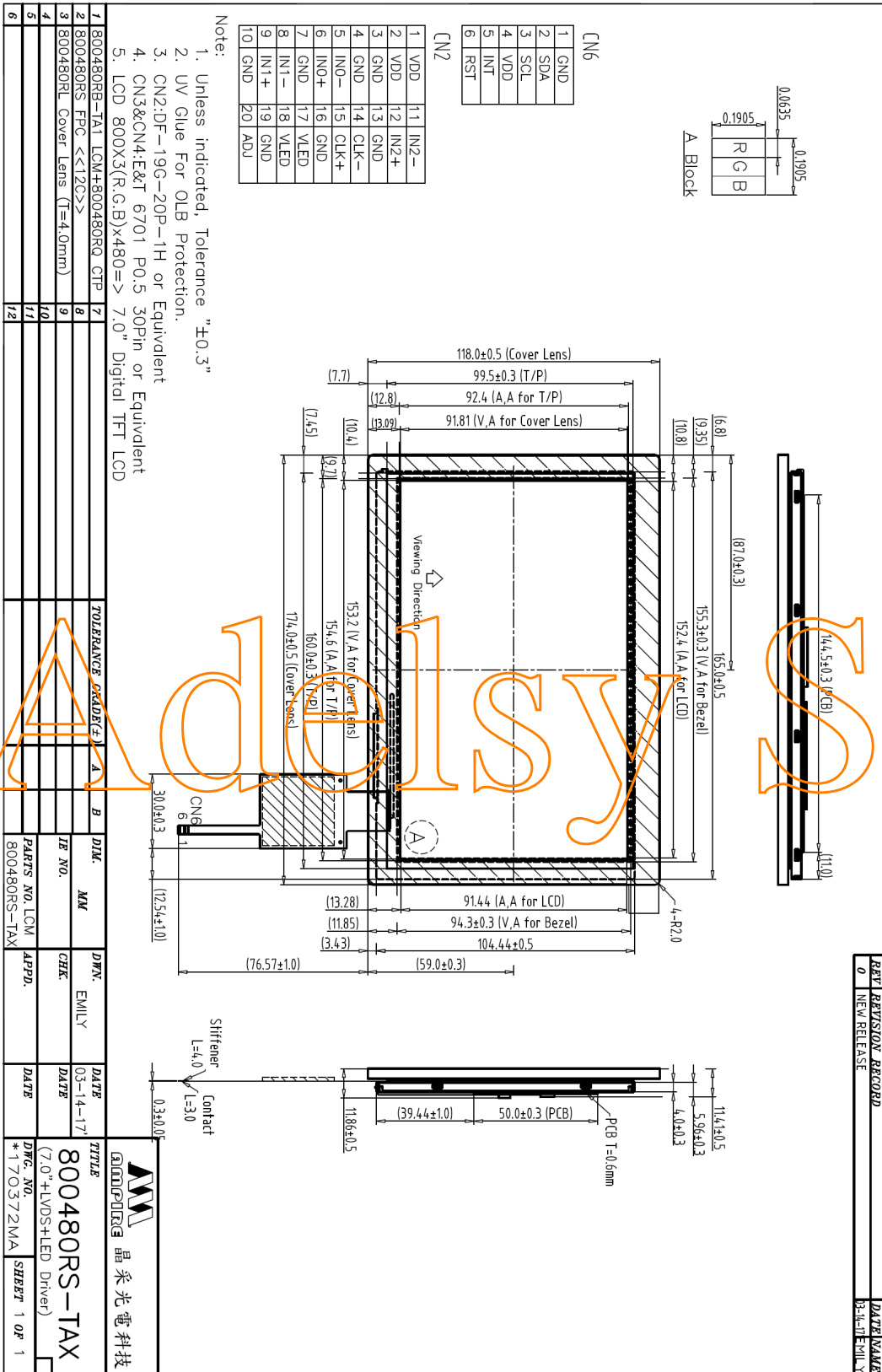
8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

### **11.5 Other**

- 1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- 2) Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver.
- 3) AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products.

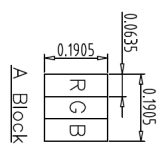
Adelsy Srl

# 12 OUTLINE DIMENSION



# adesis Sri

REV	REVISION RECORD	DATE	NAME
0	NEW RELEASE	16-11-17	EMILY Y



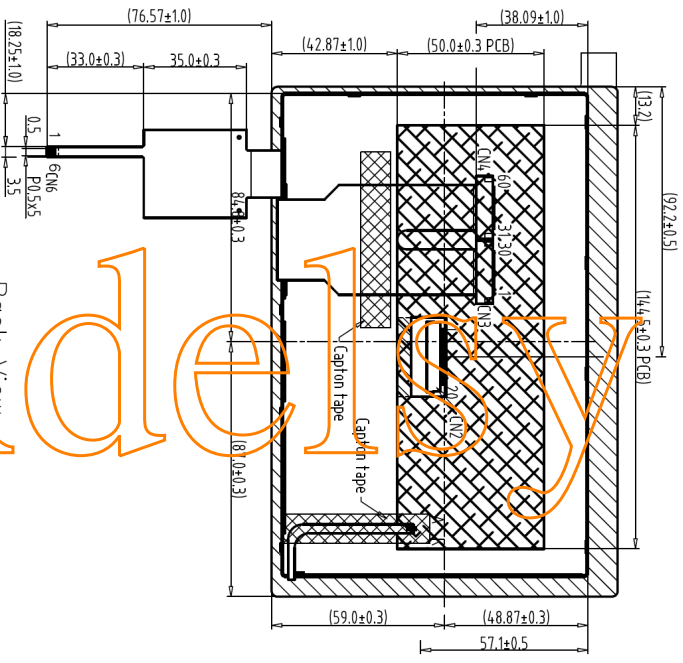
CN6

1	GND
2	SDA
3	SCL
4	VDD
5	INT
6	RST

CN2

1	VDD	11	IN2-
2	VDD	12	IN2+
3	GND	13	GND
4	GND	14	CLK-
5	IND-	15	CLK+
6	IND+	16	GND
7	GND	17	VLED
8	IN1-	18	VLED
9	IN1+	19	GND
10	GND	20	ADJ

Note:  
 1. Unless indicated, Tolerance "±0.3"  
 2. UV Glue For OLB Protection.  
 3. CN2:DF-19G-20P-1H or Equivalent  
 4. CN3&CN4:E&T 6701 P0.5 30Pin or Equivalent  
 5. LCD 800X3(R.G.B)X480=> 7.0" Digital TFT LCD



Back View

1	800480RS-TAX	LCM+800480RQ_CTP	7	TOLERANCE GRAD(F)	A	B	DIA.	MM	DWN.	EMILY	DATE	03-14-17
2	800480RS	FPG <<12C>>	8								DATE	
3	800480RL	Cover Lens (I=4.0mm)	9								DATE	
4			10								DATE	
5			11								DATE	
6			12								DATE	

**AMP**  
**AMP** 晶采光電科技  
**800480RS-TAX**  
 (7.0"+LVDS+LED Driver)  
 Dwg. No. \*170373MA  
 SHEET 1 OF 1